

Fig. 1A

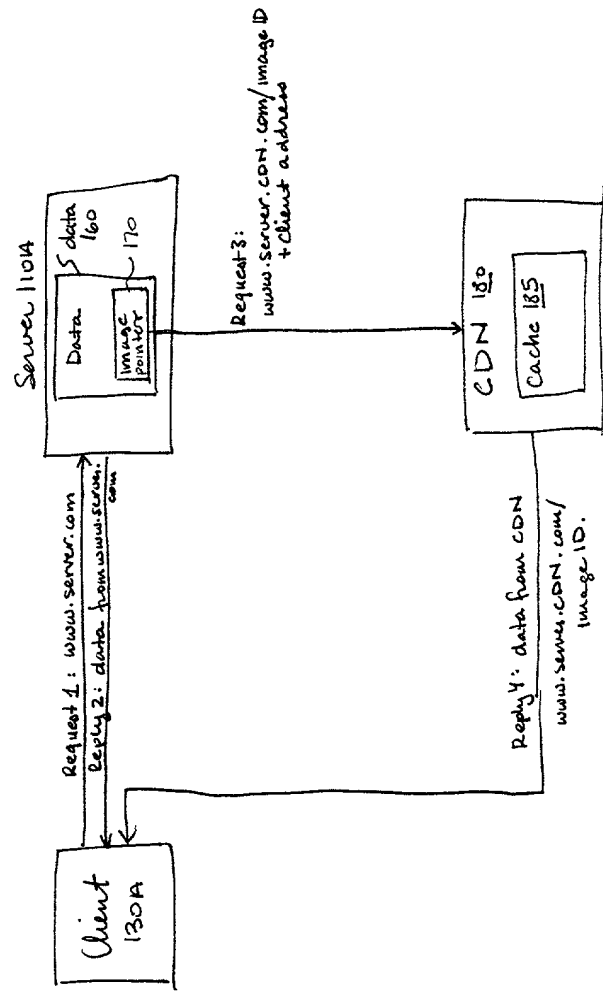


Fig. 1B

FIG. 2 is a block diagram of a computer system in accordance with the present invention.

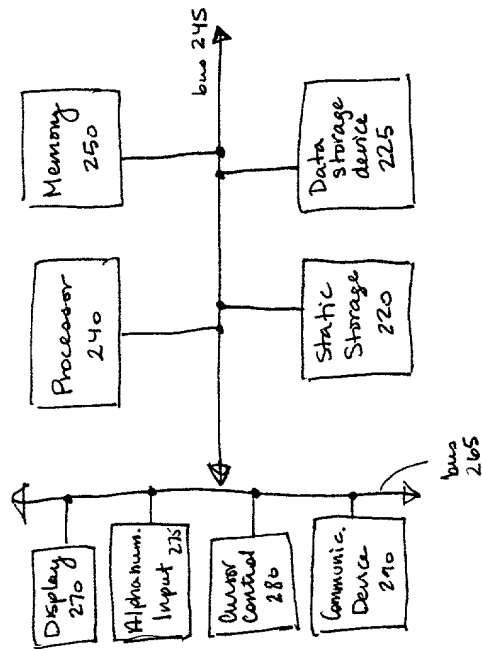


Fig. 2

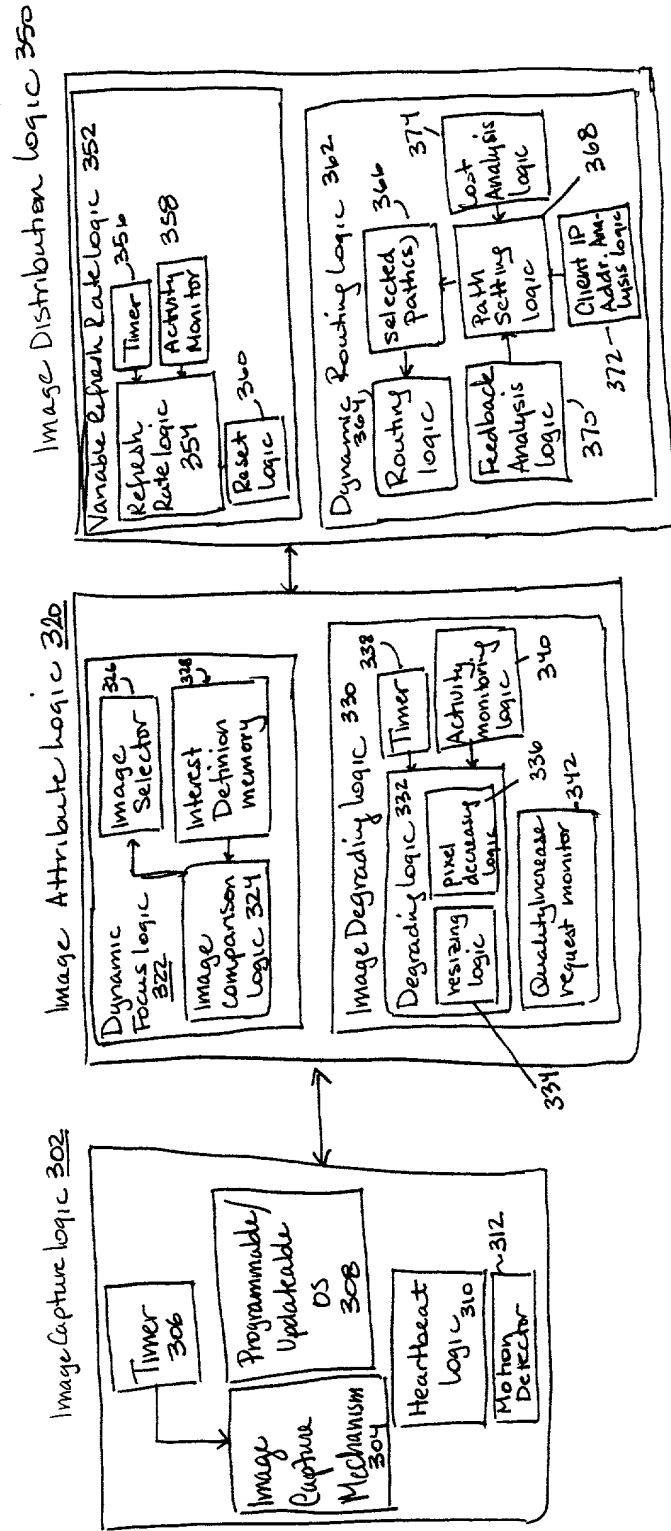


Fig 3

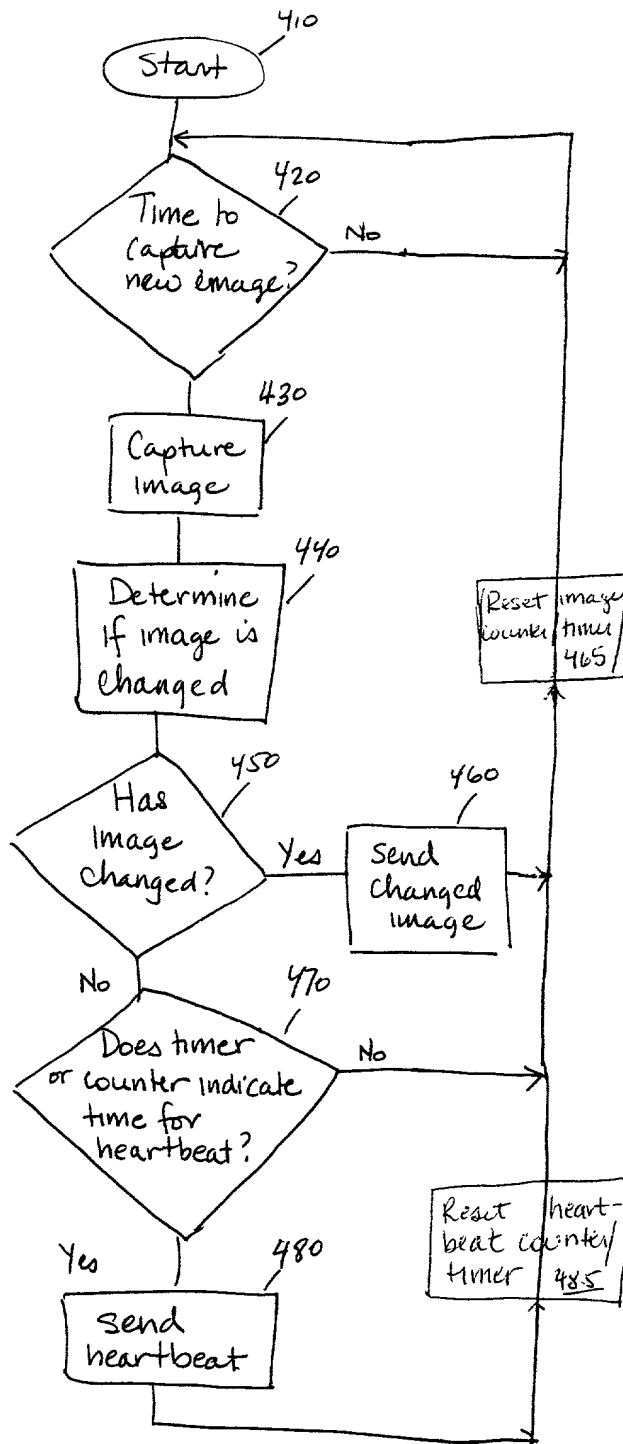


Fig 4

Patent 6,344,661

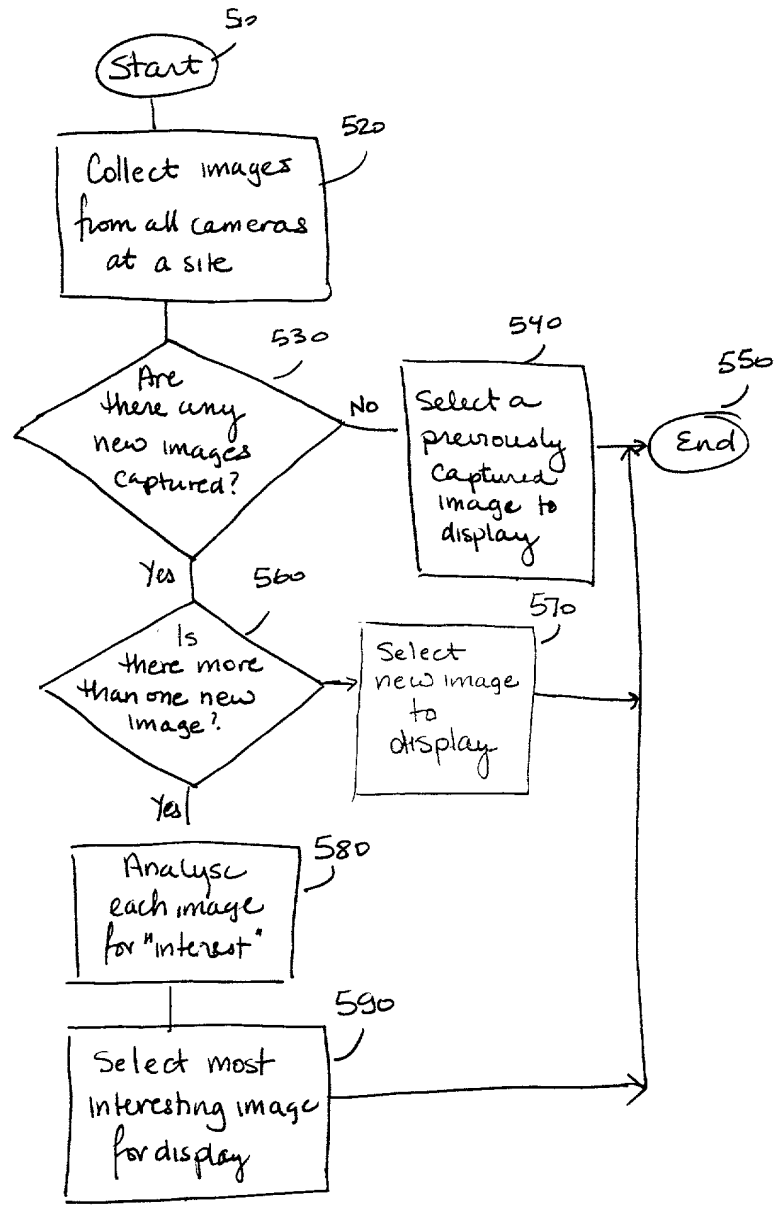


Fig 5

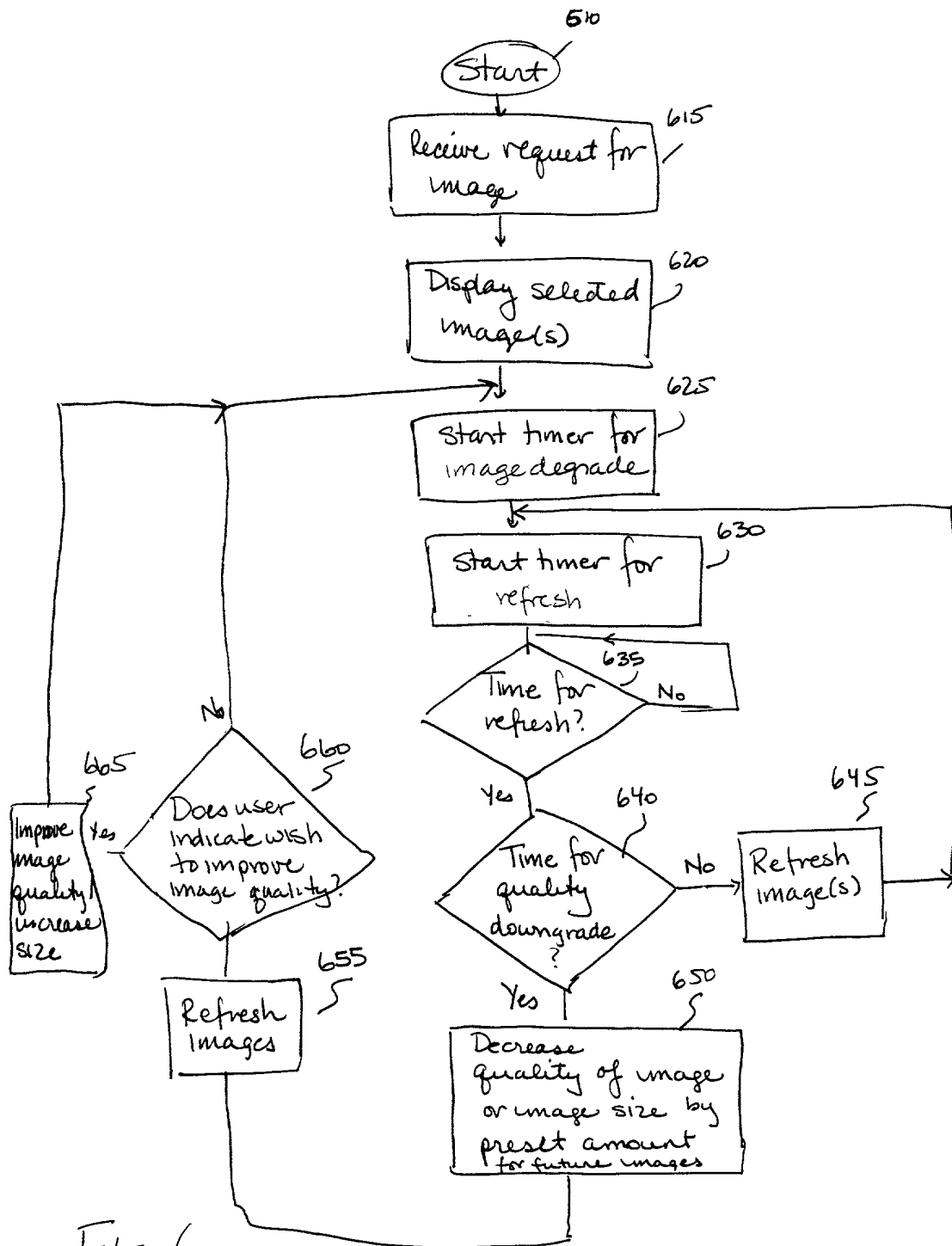


Fig 6

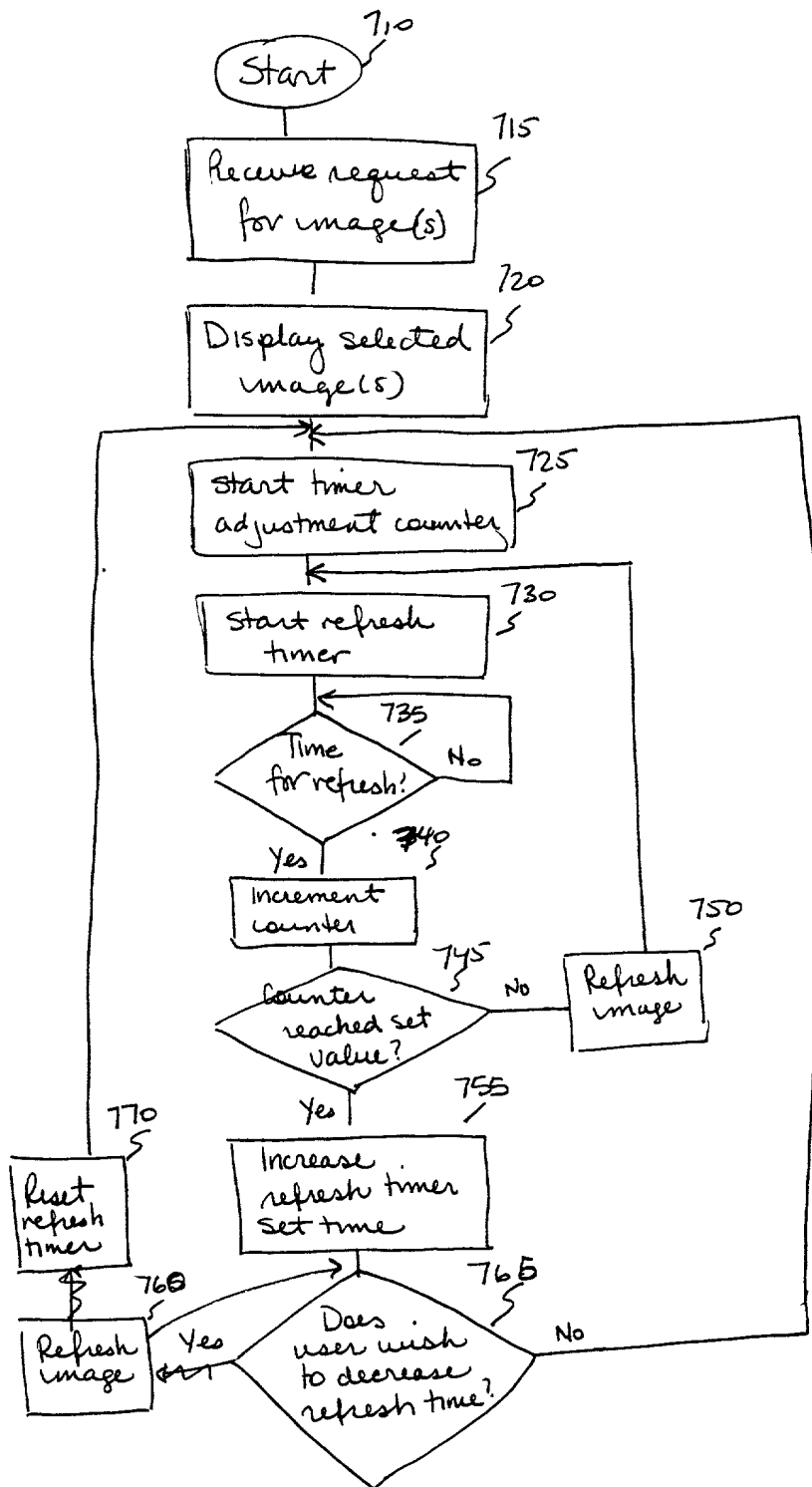


Fig 7



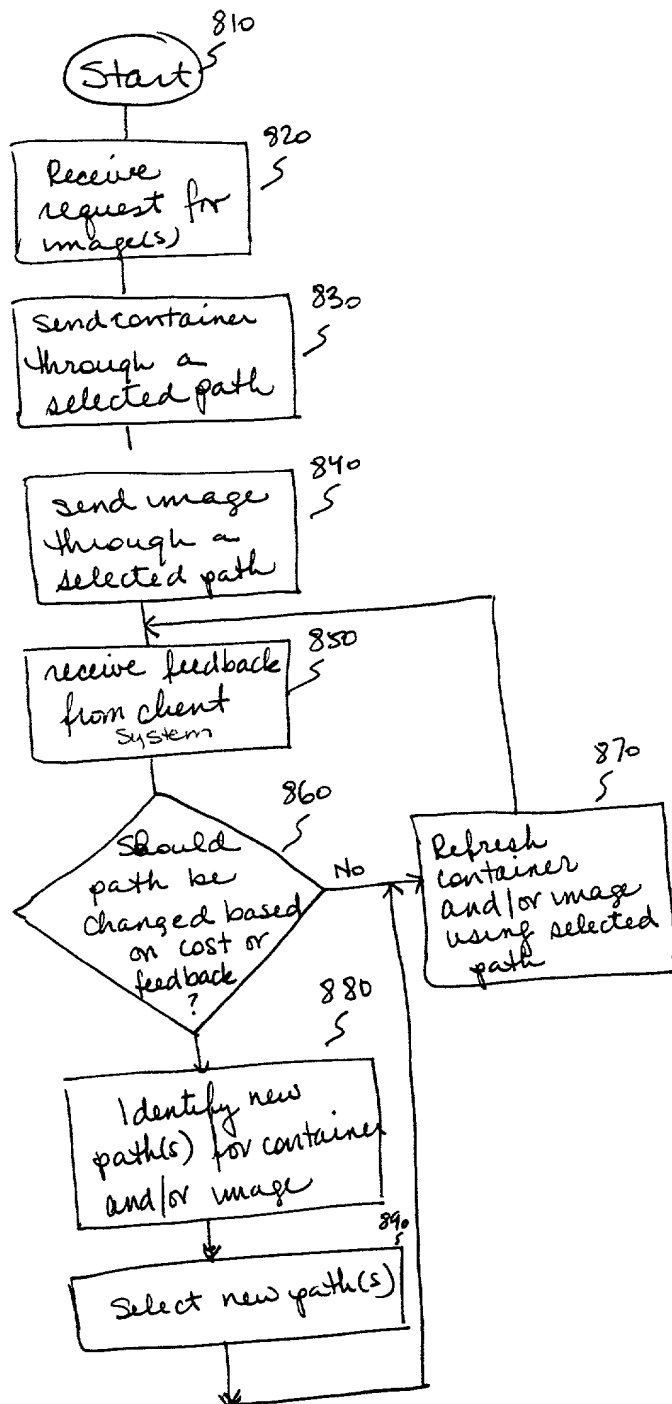


Fig. 8